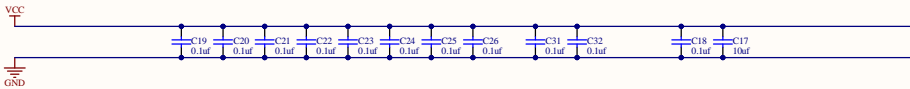
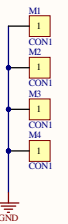


- MD7 0 8bit BIOS
1 16bit BIOS
- MD6 0 ROM disable
1 ROM enable
- MD5 0 I/O port at 2xx
1 I/O port at 3xx
- MD4 0 MCA bus
1 PC bus
- MD3-0* External switch settings
- RMD7 0 8bit video memory access
1 16bit video memory access
- RMD6 0 Internal clock chip enable
1 External clock chip disable
- RMD5 0 256Kx4 memory (4chips)
1 256Kx4 memory (2chips)
- RMD4 0 selects 46E8 for port control
1 selects 3C3 for port control
- RMD3 0 24K BIOS
1 32K BIOS
- RMD1 0 Enables ISA/MCA bus interface
1 Reserved
- RMD0 0 Uses SA lines to decode MCS16*
1 Uses LSA19-17 to decode MCS16*



Title		
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